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CB45 U. **UTILITY PATENT APPLICATION TRANSMITTAL**  
**(Large Entity)**

*(Only for new nonprovisional applications under 37 CFR 1.53(b))*

Docket No.

**Total Pages in this Submission**

44

2

**TO THE ASSISTANT COMMISSIONER FOR PATENTS**

**Box Patent Application**  
**Washington, D.C. 20231**

Transmitted herewith for filing under 35 U.S.C. 111(a) and 37 C.F.R. 1.53(b) is a new utility patent application for invention entitled:

## THREAD DISPATCHER FOR MULTI-THREADED COMMUNICATION LIBRARY

and invented by:

William G. Tuel, Jr. and Rama K. Govindaraju

If a **CONTINUATION APPLICATION**, check appropriate box and supply the requisite information:

Continuation  Divisional  Continuation-in-part (CIP) of prior application No.:

### Which is a:

Continuation    Divisional    Continuation-in-part (CIP) of prior application No.: \_\_\_\_\_

Which is a:

Continuation    Divisional    Continuation-in-part (CIP) of prior application No.:

Enclosed are:

## Application Elements

1.  Filing fee as calculated and transmitted as described below
  
2.  Specification having 24 pages and including the following:
  - a.  Descriptive Title of the Invention
  - b.  Cross References to Related Applications (*if applicable*)
  - c.  Statement Regarding Federally-sponsored Research/Development (*if applicable*)
  - d.  Reference to Microfiche Appendix (*if applicable*)
  - e.  Background of the Invention
  - f.  Brief Summary of the Invention
  - g.  Brief Description of the Drawings (*if drawings filed*)
  - h.  Detailed Description
  - i.  Claim(s) as Classified Below
  
  - j.  Abstract of the Disclosure

**UTILITY PATENT APPLICATION TRANSMITTAL**  
**(Large Entity)**

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POU919990100US1

Total Pages in this Submission  
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**Application Elements (Continued)**

3.  Drawing(s) (when necessary as prescribed by 35 USC 113)  
a.  Formal Number of Sheets 6  
b.  Informal Number of Sheets \_\_\_\_\_

4.  Oath or Declaration  
a.  Newly executed (original or copy)  Unexecuted  
b.  Copy from a prior application (37 CFR 1.63(d)) (for continuation/divisional application only)  
c.  With Power of Attorney  Without Power of Attorney  
d.  **DELETION OF INVENTOR(S)**  
Signed statement attached deleting inventor(s) named in the prior application,  
see 37 C.F.R. 1.63(d)(2) and 1.33(b).

5.  Incorporation By Reference (usable if Box 4b is checked)  
The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied  
under Box 4b, is considered as being part of the disclosure of the accompanying application and is hereby  
incorporated by reference therein.

6.  Computer Program in Microfiche (Appendix)

7.  Nucleotide and/or Amino Acid Sequence Submission (if applicable, all must be included)  
a.  Paper Copy  
b.  Computer Readable Copy (identical to computer copy)  
c.  Statement Verifying Identical Paper and Computer Readable Copy

**Accompanying Application Parts**

8.  Assignment Papers (cover sheet & document(s))

9.  37 CFR 3.73(B) Statement (when there is an assignee)

10.  English Translation Document (if applicable)

11.  Information Disclosure Statement/PTO-1449  Copies of IDS Citations

12.  Preliminary Amendment

13.  Acknowledgment postcard

14.  Certificate of Mailing

First Class  Express Mail (Specify Label No.): **EK711552644US**

**UTILITY PATENT APPLICATION TRANSMITTAL**  
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Docket No.  
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**44**

**Accompanying Application Parts (Continued)**

15.  Certified Copy of Priority Document(s) (if foreign priority is claimed)

16.  Additional Enclosures (please identify below):

**Fee Calculation and Transmittal**

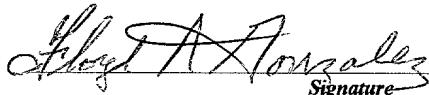
**CLAIMS AS FILED**

For	#Filed	#Allowed	#Extra	Rate	Fee
<b>Total Claims</b>	48	- 20 =	28	x \$18.00	\$504.00
<b>Indep. Claims</b>	4	- 3 =	1	x \$78.00	\$78.00
<b>Multiple Dependent Claims (check if applicable)</b>	<input type="checkbox"/>				\$0.00
				<b>BASIC FEE</b>	\$690.00
<b>OTHER FEE (specify purpose)</b>					\$0.00
				<b>TOTAL FILING FEE</b>	<b>\$1,272.00</b>

A check in the amount of \_\_\_\_\_ to cover the filing fee is enclosed.

The Commissioner is hereby authorized to charge and credit Deposit Account No. **09-0463** as described below. A duplicate copy of this sheet is enclosed.

- Charge the amount of **\$1,272.00** as filing fee.
- Credit any overpayment.
- Charge any additional filing fees required under 37 C.F.R. 1.16 and 1.17.
- Charge the issue fee set in 37 C.F.R. 1.18 at the mailing of the Notice of Allowance, pursuant to 37 C.F.R. 1.311(b).

  
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Dated: **June 6, 2000**

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Docket Number: POU919990100US1

THREAD DISPATCHER FOR MULTI-  
THREADED COMMUNICATION LIBRARY

APPLICATION FOR UNITED STATES  
LETTERS PATENT

"Express Mail" Mailing Label No.: EK711552644US  
Date of Deposit: June 6, 2000

I hereby certify that this paper is being deposited with the United States Postal Service as "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to the Box Patent Application, Assistant Commissioner for Patents, Washington, D.C. 20231.

Name: Susan L. Phelps

Signature: Susan L. Phelps

INTERNATIONAL BUSINESS MACHINES CORPORATION

## THREAD DISPATCHER FOR MULTI-THREADED COMMUNICATION LIBRARY

This invention relates to thread dispatching in a multi-threaded communication library, and more particularly relates to efficient dispatching of threads which become runnable by completion of communication events. The choice of which thread to dispatch is based on the state of the message passing system to allow for maximum efficiency of the communication infrastructure.

Background of the Invention:

In order to better understand the background of the subject invention, explanation of certain terminology is first provided. A term well-known in the art as a symmetric multi-processor (SMP) refers to an aspect of hardware in a computing system and, more particularly, relates to the physical layout and design of the processor planar itself. Such multiple processor units have, as one characteristic, the sharing of global memory as well as equal access to I/O of the SMP system.

Another term which is commonly associated with modern complex computing systems is a "thread." The term "thread" in a general sense refers merely to a simple execution path through application software and the kernel of an operating system executing with the computer. As is well understood in the art, it is commonplace for multiple such threads to be allowed per a single process image. All threads of a process share the same address space which allows for efficient communication and synchronization among the various threads of execution in the process.

A thread standard has now been incorporated into the POSIX standard (1003c.1). Basic thread management under the POSIX standard is described, for example, in a publication by K. Robbins and S. Robbins entitled Practical UNIX Programming - A Guide To Concurrency, Communication and Multi-threading, Prentice Hall PTR (1996).

Another concept which is utilized hereinafter in describing the invention is one of "thread locks" or "mutexes." It is typical in modern computing systems to include critical sections of code or shared data structures whose integrity is extremely important to the correct operation of the system. Locks/mutexes are, in general, devices employed in software (or hardware) to "serialize" access to these critical sections of code and/or shared data structures.

Two types of locks are often encountered in the art, namely blocking locks and simple or "spin" locks. Blocking locks are of the form which cause a thread requesting the lock to cease being runnable, e.g., to go to "sleep" as the term is employed in the art, if the lock is currently held by another thread. Spin locks, in contrast, do not put waiting threads to "sleep", but rather, 10 the waiting threads execute a spin loop, and thus repeatedly continue to request the lock until it is freed by the current thread "owner." Spin locks therefore continue to consume CPU cycles if the lock the thread is waiting for is owned by a different thread. Blocking locks are typically used for large critical sections of code or if the operating system kernel must differentiate between threads requiring data structure read-only capability and threads requiring the capability to 15 modify the data structure(s).

One other term to note is the concept of code being multithread-safe. Code is considered to be thread/MP-safe if multiple execution threads contending for the same resource or routine are serialized such that data integrity is insured for all threads. One way of effecting this is by 20 means of the aforementioned locks.

Presently, thread locking employs standard POSIX mutex functions. These standard POSIX functions include `thread_mutex_lock` and `thread_mutex_unlock` which are described, for example, in the above-referenced publication by K. Robbins & S. Robbins entitled Practical UNIX Programming - A Guide to Concurrency, Communication and Multi-threading. These 25 functions are designed to enhance portability of applications running on several operating systems.

A communication library is a set of functions by which processes (tasks) can send, receive, and wait for messages to/from each other. A typical communication library provides means for a receiver of a message to discriminate among possible messages that have been sent. This is often called "message matching logic."

5 In a multi-threaded communication library, multiple threads can be waiting for messages to be received from other tasks. In prior versions of the MPI library available from IBM, when a message was received, the first thread to be waiting was notified of a waiting message. It awoke and checked to see if the message was for it. If not, it awakened the next waiting thread, and so on, until the thread waiting for the specific message was awakened. The extra work in

10 awakening threads which have no work to do creates inefficiency.

15 PARALLELIZED MANAGEMENT OF ADVANCED PROGRAM-TO-PROGRAM  
COMMUNICATIONS/VM IN A SERVER SUPERSTRUCTURE, IBM Technical Disclosure Bulletin, Vol. 38, No. 02, Feb. 1995, PP 319-320, discloses running multiple threads, each thread being dispatched to handle an incoming message, the number of threads being dependent on the message rate. All threads are equivalent, and there is no binding of messages to threads.

20 MULTI-THREAD SEQUENCING IN A SMALL COMPUTER SYSTEM INTERFACE ENVIRONMENT, IBM Technical Disclosure Bulletin, Vol. 37, No. 09, Sept. 1994, PP 497-499, discloses a technique for properly sequencing commands to a multi-threaded hardware device by annotating each command with a word which indicates which other thread must complete before this thread can start. In this way, a properly ordered queue of commands can be maintained.

25 U.S. Patent No. 5,560,029 issued Sep. 24, 1996 to Papadopoulos et al. for DATA PROCESSING SYSTEM WITH SYNCHRONIZATION COPROCESSOR FOR MULTIPLE THREADS, discloses a distributed data flow computer, in which the threads are the sequences of machine instructions which are queued and assigned to any available machine processor without distinction. The patent focuses especially on handling reads of remote memory, in which a

thread's next instruction is not queued until the remote memory request is satisfied. This enqueueing is done by hardware, and not assigned to any specific processor.

U.S. Patent No. 5,784,615 issued Jul. 21, 1998 to Lipe et al. for COMPUTER SYSTEM MESSAGING ARCHITECTURE, discloses a mechanism for passing messages between the various protection zones in the Windows 95 operating system. In the patent, "thread" is to be interpreted as sequence of machine instructions, and not the POSIX thread construct. The focus of the patent is on providing messaging services between secure and insecure domains of the operating system, by providing callback functions in the secure domain that can be invoked by a user in the insecure domain. There is no notion of thread synchronization or special dispatching techniques, other than a general mention of using a standard semaphore to allow two threads to cooperate.

U.S. Patent No. 5,758,184 issued May 26, 1998 to Lucovsky et al. for SYSTEM FOR PERFORMING ASYNCHRONOUS FILE OPERATIONS REQUESTED BY RUNNABLE THREADS BY PROCESSING COMPLETION MESSAGES WITH DIFFERENT QUEUE THREAD AND CHECKING FOR COMPLETION BY RUNNABLE THREADS, discloses a technique for performing multiple simultaneous asynchronous input/output operations in a Computer Operating System. The focus of the patent is efficiently handling completion of I/O operations using threads.

U.S. Patent. No. 5,710,923 issued Jan. 20, 1998 to Jennings et al. for METHODS AND APPARATUS FOR EXCHANGING ACTIVE MESSAGES IN A PARALLEL PROCESSING COMPUTER SYSTEM, discloses a method for communicating active messages among nodes of a parallel processing computer system where an active message comprises a pointer to a function to be invoked at the target when the message arrives at the target with a few parameters from the message being passed to the function upon arrival.

U.S. Patent No. 5,548,760 issued Aug. 20, 1996 to Healey for MESSAGE HANDLER, discloses a message handler for passing messages between processes in a single threaded operating system.

It is typical for a message passing library to provide a reliable transport mechanism for 5 messages between tasks, a mechanism known in the art as “flow control” is incorporated. The flow control mechanism requires state to be maintained both at the sender and receiver of messages to ensure a reliable transport can occur. If messages are lost in transit they are retransmitted by the sender based on the state maintained. The flow control mechanism bounds the amount of state that needs to be maintained to guarantee the reliability of message delivery.

10 The bounded state is also sometimes referred to in the art as the flow control window. The size of the window is referred to in the art as tokens. Tokens are used up when messages are sent and are freed when the receiver acknowledges them thus advancing the window. A critical design aspect for high performance message passing design systems is to ensure that the sending of messages and acknowledgments is tuned such that a sender is not blocked due to lack of tokens. In a multi-threaded message passing system where several threads are waiting for messages to arrive and then send acknowledgments for freeing tokens, it is critical for the message passing system to be able to dispatch the thread that is most likely to minimize senders being blocked due to tokens. Efficient message passing systems therefore cannot simply rely on POSIX thread dispatch routines for efficient dispatch since the state to decide which thread to be dispatched for maximum efficiency is in the message passing system and not in POSIX utility functions.

25 Certain messages in multiprocessor message passing systems are more critical than others, for example, messages that typically deal with distributed lock manager in databases and file systems. It is more efficient to dispatch threads that process these performance critical messages before handling other messages. The ability to recognize certain messages as being more critical and dispatching the appropriate threads to process them is critical for efficient message passing systems.

The above examples show how state can be maintained efficiently in the message passing system to allow controlled thread dispatching for maximum efficiency. Our invention described in this disclosure details an efficient mechanism by which the messaging system can control the dispatching of messaging threads to enhance its performance.

5 Summary of the Invention:

In the present invention, each thread has a thread-specific structure containing a “ready flag” and a POSIX thread condition variable unique to that thread. Each message is assigned a “handle.” When a thread waits for a message, a thread-specific structure is attached to the message handle being waited on, and the thread is enqueued, waiting for its condition variable to be signaled. When a message completes (i.e., arrives, is matched, and is copied into the user buffer), the message matching logic sets the ready flag to READY, and causes the queue to be examined. The queue manager scans the queue of waiting threads, and sends a thread awakening condition signal to one of the threads with its ready flag set to READY. The queue manager can implement any desired policy, including First-In-First-Out (FIFO), Last-In-First-Out (LIFO), or some other thread priority scheduling policy. This ensures that the thread which is awakened has the highest priority message to be processed, and enhances the efficiency of message delivery. The priority of the message to be processed is computed based on the overall design of the message passing library, and can include giving priority to flow control messages as described in the examples given above.

20 These and other objects will be apparent to one skilled in the art from the following drawings and detailed description of the invention.

Brief Description of the Drawings:

Fig. 1 depicts one example of a threaded computer environment usable with the present invention;

Fig. 2 is a representation of a queue for storing a plurality of Thread Queue Elements (TQE) therein;

Fig. 3 is a representation of a base structure or Pipe-control containing information shared by all threads;

5 Figs. 4A and 4B, joined by connectors a-a, form a flowchart of a program of the present invention for controlling multi-threaded communications;

Fig. 5 is a representation of a message handle structure, with fields for message source, tag, buffer address, maximum length, a “waited on” flag, and a notify address;

Fig. 6 illustrates the relationship of the elements of the invention at one point in the operation; and

Fig. 7 is a graph showing the improved performance of multithread communications using the present invention over the method used under the prior art.

Description of the Preferred Embodiment:

As shown in Fig. 1, a computer environment 100 includes a plurality of computing nodes 102 coupled to one another via a connection 104. As one example, each computing node may comprise a node of an RS/6000 SP System offered by International Business Machines Corporation, and connection 104 may be a packet switch network, such as the SP switch or high performance switch (HPS), also offered by International Business Machines Corporation. Note again, Fig. 1 is presented by way of example only. The techniques disclosed herein could apply 20 to any serial program or any multithreaded program running on a single machine in addition to the multi-processor environment depicted in Fig. 1.

Within environment 100, message packets are passed from a source computing node (sender) to a receiver computing node (receiver) via packet switch network 104. For example, a user task 106 of computing unit N may pass a message to a user task 106 of computing unit 1 (receiver). Each user task can directly read data from and write data to an associated adapter 112, bypassing the overhead normally associated with having the operating system intervene in communication protocols. Adapter 112 couples computing unit 102 to switch 104. One example of switch 104 is described in detail in "IBM Parallel System Support Programs For AIX Administration Guide," Publication No. GC23-3897-02 (1996).

As further explanation, communication between a computing unit and its associated adapter 112 is, for instance, described by an interface that includes functions, such as, open communication, close communication, enable route, disable route, return status, and reset adapter. In one embodiment, the interface comprises a message passing interface (MPI) 110, also referred to herein as an MPI communication library. The MPI library comprises one example of a resource for which a lock mechanism in accordance with the present invention may be employed.

The MPI library is described in greater detail in, for example, an International Business Machines Corporation publication entitled "IBM Parallel Environment For AIX: MPI Programming and Subroutine Reference," Version 2, Release 4 (October, 1998), the entirety of which is hereby incorporated herein by reference.

International Business Machines Corporation's implementation of the MPI library is described in detail in various additional publications. For example, reference an article in the IBM Systems Journal entitled "The Communication Software In Parallel Environment Of The IBM SP2," Vol. 34, No. 2, pp. 205-215 (1995). Further information on communication libraries is available in a textbook by W. Richard Stevens entitled UNIX Network Programming, published by Prentice Hall, Inc. (1990). Both of these references are hereby incorporated by reference in their entirety.

As noted, it is assumed herein that the computing environment comprises a threaded computer environment so that the user task comprises a threaded user, and the library is a threaded MPI. A threaded computer environment is today well-known in the industry as one approach to implementing multi-node distributed processing. A threaded MPI library is available from International Business Machines Corporation as "IBM Parallel Environment For AIX," Version 2, Release 4 , IBM Product No. 7565-543 (October, 1998). This threaded MPI comprises a licensed program product which runs on the AIX system. "AIX" is the IBM version of the UNIX operating system.

The system of Fig. 1 receives messages on multiple threads, and activates the correct thread to process the message. This is done by the MPI programs using the POSIX threads library, and particularly the implementation with the IBM Parallel Environment (PE) and Parallel System Support Program (PSSP) products.

As part of the invention, at the time a thread is created using standard POSIX calls, a block of storage specific to that thread, called a Thread Queue Element (TQE), is created. Fig. 2 is a representation of a TQE queue 20 storing such TQE's 10. The queue is represented by having the forward and back pointers contain addresses of other TQE's 10 in the queue. The TQE 10 has the following fields: Forward Pointer 11, Back Pointer 12, State 13, Identification (ID) 14, Queued flag 15, and Target Thread Condition structure 16. The Forward and Back Pointers 11 and 12 are used to maintain a queue of TQE's 10, using well-known linked-list processing techniques. The State 13 can be READY or WAITING. A TQE 10 that has State=READY can be dequeued at any time. A TQE 10 that has State=WAITING can only be dequeued if there are no TQE's 10 with State=READY. The Queued flag 15 is set to indicate whether the TQE 10 is part of a TQE queue 20, and the Target Thread Condition is a POSIX thread condition structure that can be waited on using the standard POSIX thread calls. The ID 14 is the POSIX thread ID used for additional user information. Initially, a TQE 10 is not enqueued in the queue 20, and has state=READY.

There is a TQE 10 for each message passing thread. In addition, there is a base structure 25 of Fig. 3, wherein the base structure is called the Pipe-control. The Pipe-control 25 has the following fields (which are accessible by and common to all threads); TQE\_queue\_head 26, TQE\_queue\_tail 27, TQE\_ready\_count 28, TQ\_key 29, and TQ\_mutex 30. The 5 TQE\_queue\_head 26 and TQE\_queue\_tail 27 are pointers to the head and tail, respectively, of the TQE queue 20. The TQE\_ready\_count is the count of the number of TQE's in the TQE queue 20 with state=READY. The TQ\_key 29 is a value used to obtain the TQE 10 for the currently-running thread, and is a well-known part of the thread-specific storage functionality of POSIX threads. The TQ\_mutex is a POSIX mutex, used to serialize access to the pipe\_control 10 structure 25 and the TQE queue 20. Such serialization is required because the elements in Pipe-control 25 are accessed and modified by more than one thread.

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Figs. 4A and 4B, joined at connectors a-a, form a flowchart of the message processing logic of the present invention. At 31, a thread wants to receive a message and starts the program. At 32 a buffer large enough to contain the message being received is allocated. At 34, a handle is allocated for the message (Fig. 5). The handle contains information such as the buffer address, the match condition (to be discussed), whether the message has been “waited on,” and the address of a TQE 10 to be “notified” when a message matching the match conditions has been received and copied into the buffer allocated at 32. Other than the TQE, this handle structure has been used by PSSP in all prior versions of the MPC1/MPI library. At 36, the handle is enqueued on an unmatched message queue 40. The list (or queue) 40 is a list of handles for which buffers have been allocated and match conditions posted, but a message satisfying these conditions has not yet been received. When the handle is enqueued in 40, the waited-on flag for that entry is set to 0, and the thread-notify TQE address for that entry is set to NULL, since at this point the user has not indicated a desire to wait for the message. At 37, the program waits for a message to be 25 received.

At 37, a user decides to wait on a handle. That is, some thread will wait for a message to be received that matches the conditions listed in the particular handle passed by the message passing logic. The TQE 10 for that thread will be obtained (via the pipe\_control 25 TQ\_key 29)

and the state 13 set to READY at 38. At 40, the thread gets access to the internal message passing logic via a call to MPID\_lock, which returns when the thread “owns” the message passing lock. The lock/unlock process is fully discussed in the patent application for SYSTEM FOR RESOURCE LOCK/UNLOCK CAPABILITY IN MULTITHREADED COMPUTER  
5 ENVIRONMENT by Govindaraju et al., Serial Number 09/139,255 filed 08/25/98 (Attorney Docket No. PO9-98-144), incorporated herein by reference.

At 42, the handle waited-on flag is set to 1, meaning that the message is now being waited on. At 44, the internal message passing logic (routine) is called. This routine reads any incoming messages and tries to match them with the match conditions on all the handles in the  
10 unmatched queue. If a message matches, the data is copied into the user’s buffer, and the thread-notify TQE address in the handle (if set), is used to identify the thread to be restarted; the state of the thread-notify TQE is set to READY, and the count of READY TQE’s 28 is updated in the Pipe\_control structure 25.

At 46 of Fig. 4B, when the internal message routine returns, the thread checks to see if the message it was waiting for was one of the messages that was matched. If yes, the handle is freed at 48, and unlocks the internal message passing logic at 50. The message reception is now complete, and the thread continues at 52 to do other work.

If at 46, the handle being waited on by this thread was not matched, then at 54, the thread will prepare to wait. It does this by setting the state=WAITING for its TQE, and putting the  
20 address of its TQE in the handle as the thread-notify address. At 56, a check is made to determine if the message handle for this thread is matched. If yes, the message is complete, the handle is freed at 48, the locked released at 50, and the thread continues on with other work at 52.

If the message handle is not matched at 56, a check is made at 58. At 58, the thread tests  
25 the Pipe\_control TQ\_ready count 28 to see if any threads are ready to run, or if its time slice has expired. Time slicing is well understood by those skilled in the art, and will not be discussed

5 further. At 60, the thread calls MPID\_unlock to unlock the internal message passing routine.. This restarts the first READY TQE in the TQE queue 20 by sending a thread signal to its TQE target signal condition. At 62, the thread calls the system call “yield,” allows any restarted threads to be assigned to a processor and do useful work. Once this thread has been given  
10 control back from the operating system, it calls MPID\_lock at 64. MPID\_lock causes the enqueueing of the TQE and waiting for a signal to its TQE thread signal condition (Fig. 6). Thus, this thread will not return from MPID\_lock called at 64 until it has been signaled, and it doesn’t get signaled until it is READY (i.e. has a message matched), (or until there are no READY TQE’s). Thus, this thread will sleep until a message arrives that matches the conditions set, and will not be restarted prematurely, even if it was the first thread to wait for a message.

10 Once this thread gets control back from-MPID\_lock, at 66 the thread will call the internal message passing routine to read messages and try to match them against any posted handle. The thread then loops back to 56, where it expects to find the message matched and thus finish via 48-52. The MPID\_lock/unlock routines are as follows:

MPID\_lock:

- a) get TQE element for this thread via Pipe\_control TQ\_key;
- b) lock the pipe\_control.mutex lock;
- c) enqueue the TQE on the TQE thread queue using standard linked-list management for the forward and back pointers;
- 20 d) while Pipe\_control\_owner = 0, wait for the TQE Target signal condition. This is the point at which the thread will wait until a message arrives;
- e) claim lock ownership by setting Pipe\_control\_owner = TQE\_id (14);
- f) dequeue the TQE, since it no longer is waiting for the lock; and
- g) unlock the Pipe\_control\_mutex lock.

MPID\_unlock:

- a) lock the Pipe\_control\_mutex lock;
- b) search the TQE queue and find the first TQE with state=READY, (or the first element if there are no READY TQE's);
- 5 B1) if the TQE element also contains a priority field, find the highest priority TQE with state=READY;
- c) send a thread condition signal to the Target contained in the TQE selected;
- d) relinquish lock ownership by setting Pipe\_control\_owner = 0; and
- e) unlock the pipe\_control.mutex lock.

10 Fig. 7 is a graph showing the improved performance of multithread message communication using the present invention over the method used under the prior art. The prior art method is shown by curve 80, and the message multithread message communication of the present invention is shown at 82. It will be understood that the present method results in an increased bandwidth of about 100%.

15 While the preferred embodiment of the invention has been illustrated and described herein, it is to be understood that the invention is not limited to the precise construction herein disclosed, and the right is reserved to all changes and modifications coming within the scope of the invention as defined in the appended claims.

Claims:

What is claimed is:

- 1 1. A method for efficiently dispatching threads awaiting messages in a multi-threaded  
2 communication library comprising:  
3        preassigning threads to messages to be received;  
4        putting to sleep, those threads whose assigned messages have not been received;  
5        upon receipt of a message, awakening its preassigned thread; and  
6        executing said awakened thread, thereby processing the received message.
  
- 1 2. The method of claim 1 wherein the selection of the thread to be dispatched is based  
2 on its priority as set when the thread is put to sleep.
  
- 1 3. The method of claim 1 wherein said preassigning threads step comprises:  
2        creating a thread-specific structure for each thread, each thread-specific structure  
3        having a ready flag and a condition variable unique to its preassigned thread;  
4        creating a handle for each message to be received; and  
5        having a thread invoke message passing logic for a particular handle, thereby  
6        associating the thread and the message.
  
- 1 4. The method of claim 3 wherein said putting to sleep step comprises:  
2        enqueueing for a received message, a preassigned thread-specific structure into a first  
3        queue;  
4        writing into said handle associated with the message received, an identification of  
5        said thread-specific structure enqueued for the received message, and  
6        placing said thread-specific structure for the received message in the WAIT  
7        condition.

1 5. The method of claim 4 wherein said awakening step comprises;  
2 completing said received message;  
3 changing the condition of the thread-specific structure for the completed received  
4 structure to the READY condition; and  
5 dequeuing with a queue manager, the next thread-specific structure in said first  
6 queue in the READY condition and sending its thread a thread awakening condition  
signal.

1 6. The method of claim 5 further comprising;  
2 allocating in said preassigning step, buffer space for storing messages to be  
3 received; and  
4 in said putting to sleep step, identifying in said handle the buffer in which the  
5 message associated with the handle is to be stored when it is received.

1 7. The method of claim 6 wherein said completing said received message comprises  
2 storing said received message in the buffer identified in the associated handle for the  
3 received message.

1 8. The method of claim 5 wherein said queue manager dequeues the next  
2 thread-specific structure using a First-In-First-Out policy.

1 9. The method of claim 5 wherein said queue manager dequeues the next  
2 thread-specific structure using a Last-In-First-Out policy.

1 10. The method of claim 5 wherein said queue manager dequeues the next  
2 thread-specific structure based on a priority value contained in said structure.

1 11. The method of claim 5 further comprising obtaining a lock for the handle  
2 associated with said received message such that the awakened thread may process only the  
3 received message.

1 12. The method of claim 11 further comprising releasing said lock after said awakened  
2 thread has processed said received message such that said awakened thread may continue  
3 with other work.

1 13. A computer program product comprising a computer useable medium having  
2 computer readable program code means therein for efficiently dispatching threads  
3 awaiting messages in a multi-threaded communication library, said computer readable  
4 program code means in said computer program product comprising:

5 computer readable program code means for preassigning threads to messages to be  
6 received;

7 computer readable program code means for putting to sleep, those threads whose  
8 assigned messages have not been received;

9 computer readable program code means for, upon receipt of a message, awakening  
10 its preassigned thread; and

11 computer readable program code means for executing said awakened thread,  
12 thereby processing the received message.

1 14. The computer program product of claim 13 wherein the selection of the thread to  
2 be dispatched is based on its priority as set when the thread is put to sleep.

1 15. The computer program product of claim 13 wherein said computer readable  
2 program code means for preassigning threads comprises:

3 computer readable program code means for creating a thread-specific structure for  
4 each thread, each thread-specific structure having a ready flag and a condition variable  
5 unique to its preassigned thread;

6 computer readable program code means for creating a handle for each message to  
7 be received; and

8 computer readable program code means for having a thread invoke message  
9 passing logic for a particular handle, thereby associating the thread and the message.

1 16. The computer program product of claim 15 wherein said computer readable  
2 program code means for putting to sleep comprises:

3 computer readable program code means for enqueueing for a received message, a  
4 preassigned thread-specific structure into a first queue;

5 computer readable program code means for writing into said handle associated  
6 with the message received, an identification of said thread-specific structure enqueued for  
7 the received message, and

8 computer readable program code means for placing said thread-specific structure  
9 for the received message in the WAIT condition.

1 17. The computer program product of claim 16 wherein said computer readable  
2 program code means for awakening comprises:

3 computer readable program code means for completing said received message;

4 computer readable program code means for changing the condition of the  
5 thread-specific structure for the completed received structure to the READY condition;  
6 and

7 computer readable program code means for dequeuing with a queue manager, the  
8 next thread-specific structure in said first queue in the READY condition and sending its  
9 thread a thread awakening condition signal.

1 18. The computer program product of claim 17 further comprising;

2 computer readable program code means for allocating in said preassigning step,  
3 buffer space for storing messages to be received; and

4 said computer readable program code means for putting to sleep includes,  
5 computer readable program code means for identifying in said handle the buffer in which  
6 the message associated with the handle is to be stored when it is received.

1 19. The computer program product of claim 18 wherein said computer readable  
2 program code means for completing said received message comprises computer readable

3 program code means for storing said received message in the buffer identified in the  
4 associated handle for the received message.

1 20. The computer program product of claim 17 wherein said queue manager includes  
2 computer readable program code means for dequeuing the next thread-specific structure  
3 using a First-In-First-Out policy.

1 21. The computer program product of claim 17 wherein said queue manager includes  
2 computer readable program code means for dequeuing the next thread-specific structure  
3 using a Last-In-First-Out policy.

1 22. The computer program product of claim 17 wherein said queue manager includes  
2 computer readable program code means for dequeuing the next thread-specific structure  
3 based on a priority value contained in said structure.

1 23. The computer program product of claim 17 further comprising computer readable  
2 program code means for obtaining a lock for the handle associated with said received  
3 message such that the awakened thread may process only the received message.

1 24. The computer program product of claim 23 further comprising computer readable  
2 program code means for releasing said lock after said awakened thread has processed said  
3 received message such that said awakened thread may continue with other work.

1 25. An apparatus for efficiently dispatching threads awaiting messages in a  
2 multi-threaded communication library comprising:  
3 means for preassigning threads to messages to be received;  
4 means for putting to sleep, those threads whose assigned messages have not been  
5 received;  
6 means for, upon receipt of a message, awakening its preassigned thread; and  
7 executing said awakened thread, thereby processing the received message.

1 26. The apparatus of claim 25 wherein the selection of the thread to be dispatched is  
2 based on its priority as set when the thread is put to sleep.

1 27. The apparatus of claim 25 wherein said means for preassigning threads comprises:  
2 means for creating a thread-specific structure for each thread, each thread-specific  
3 structure having a ready flag and a condition variable unique to its preassigned thread;  
4 means for creating a handle for each message to be received; and  
5 means for having a thread invoke message passing logic for a particular handle,  
6 thereby associating the thread and the message.

1 28. The apparatus of claim 27 wherein said means for putting to sleep comprises:  
2 means for enqueueing for a received message, a preassigned thread-specific structure  
3 into a first queue;  
4 means for writing into said handle associated with the message received, an  
5 identification of said thread-specific structure enqueued for the received message, and  
6 means for placing said thread-specific structure for the received message in the  
7 WAIT condition.

1 29. The apparatus of claim 28 wherein said means for awakening comprises;  
2 means for completing said received message;  
3 means for changing the condition of the thread-specific structure for the completed  
4 received structure to the READY condition; and  
5 means for dequeuing with a queue manager, the next thread-specific structure in  
6 said first queue in the READY condition and sending its thread a thread awakening  
7 condition signal.

1 30. The apparatus of claim 29 further comprising;  
2 means for allocating in said preassigning step, buffer space for storing messages to  
3 be received; and

in said means for putting to sleep, means for identifying in said handle the buffer in which the message associated with the handle is to be stored when it is received.

The apparatus of claim 30 wherein said means for completing said received message comprises means for storing said received message in the buffer identified in the associated handle for the received message.

The apparatus of claim 29 wherein said queue manager includes means for dequeuing the next thread-specific structure using a First-In-First-Out policy.

The apparatus of claim 29 wherein said queue manager includes means for dequeuing the next thread-specific structure using a Last-In-First-Out policy.

The apparatus of claim 29 wherein said queue manager includes means for dequeuing the next thread-specific structure based on a priority value contained in said structure.

The apparatus of claim 29 further comprising means for obtaining a lock for the handle associated with said received message such that the awakened thread may process only the received message.

The apparatus of claim 35 further comprising means for releasing said lock after said awakened thread has processed said received message such that said awakened thread may continue with other work.

An apparatus comprising:  
a data processing system;  
a multi-threaded communication library in said data processing system;  
a thread dispatcher in said data processing system for efficiently dispatching  
threads awaiting messages in said multi-threaded communication library;

computer code which preassigns threads to messages to be received;  
computer code which puts to sleep those threads whose assigned messages have  
not been received;  
computer code which, upon receipt of a message, awakens its preassigned thread;  
and  
computer code which executes said awakened thread, thereby processing the  
received message.

The apparatus of claim 37 wherein the selection of the thread to be dispatched is based on its priority as set when the thread is put to sleep.

The apparatus of claim 37 wherein said computer code which preassigns threads comprises:

computer code which creates a thread-specific structure for each thread, each thread-specific structure having a ready flag and a condition variable unique to its preassigned thread;

computer code which creates a handle for each message to be received; and computer code which causes a thread invoke message passing logic for a particular handle, thereby associating the thread and the message.

The apparatus of claim 39 wherein said computer code which puts to sleep comprises:

computer code which enqueues for a received message, a preassigned thread-specific structure into a first queue;

computer code which writes into said handle associated with the message received, an identification of said thread-specific structure enqueued for the received message, and

computer code which places said thread-specific structure for the received message in the WAIT condition.

1 41. The apparatus of claim 40 wherein said computer code which awakens comprises;  
2 computer code which completes said received message;  
3 computer code which changes the condition of the thread-specific structure for the  
4 completed received structure to the READY condition; and  
5 computer code which dequeues with a queue manager, the next thread-specific  
6 structure in said first queue in the READY condition and sending its thread a thread  
7 awakening condition signal.

1 42. The apparatus of claim 41 further comprising;  
2 in said computer code which preassigns, computer code which allocates buffer  
3 space for storing messages to be received; and  
4 in said computer code which puts to sleep, computer code which identifies in said handle  
5 the buffer in which the message associated with the handle is to be stored when it is  
6 received.

1 43. The apparatus of claim 42 wherein said computer code which completes said  
2 received message comprises computer code which stores said received message in the  
3 buffer identified in the associated handle for the received message.

1 44. The apparatus of claim 41 wherein said queue manager includes computer code  
2 which dequeues the next thread-specific structure using a First-In-First-Out policy.

1 45. The apparatus of claim 41 wherein said queue manager includes computer code  
2 which dequeues the next thread-specific structure using a Last-In-First-Out policy.

1 46. The apparatus of claim 41 wherein said queue manager includes computer code  
2 which dequeues the next thread-specific structure based on a priority value contained in  
3 said structure.

47. The apparatus of claim 41 further comprising computer code which obtains a lock for the handle associated with said received message such that the awakened thread may process only the received message.

48. The apparatus of claim 47 further comprising computer code which releases said lock after said awakened thread has processed said received message such that said awakened thread may continue with other work.

## THREAD DISPATCHER FOR MULTI-THREADED COMMUNICATION LIBRARY

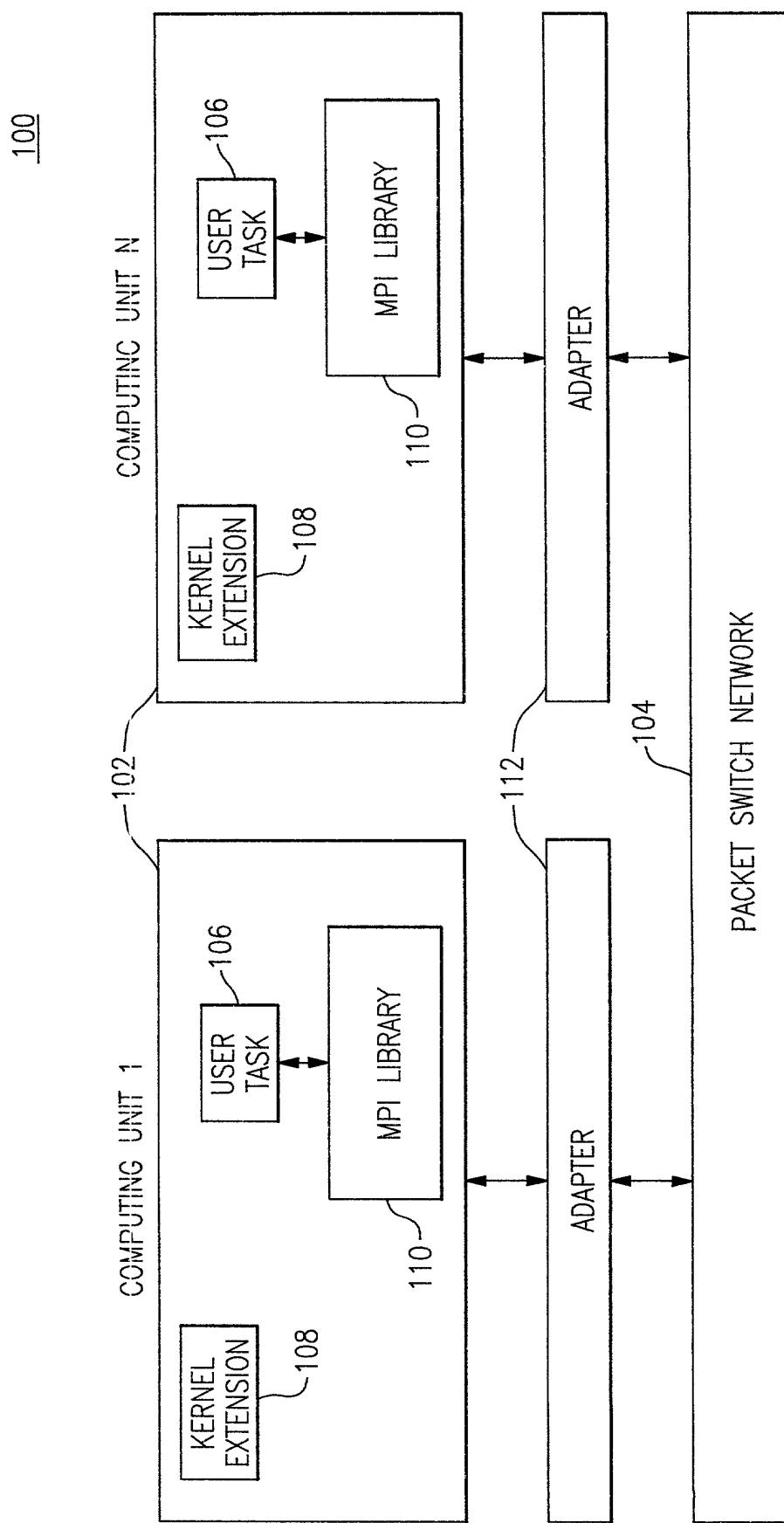
Abstract of the Disclosure:

Method, computer program product, and apparatus for efficiently dispatching threads in a multi-threaded communication library which become runnable by completion of an event. Each

5 thread has a thread-specific structure containing a “ready flag” and a POSIX thread condition variable unique to that thread. Each message is assigned a “handle”. When a thread waits for a message, thread-specific structure is attached to the message handle being waited on, and the thread is enqueued, waiting for its condition variable to be signaled. When a message completes, the message matching logic sets the ready flag to READY, and causes the queue to be examined.

10 The queue manager scans the queue of waiting threads, and sends a thread awakening condition signal to one of the threads with its ready flag set to READY. The queue manager can implement any desired policy, including First-In-First-Out (FIFO), Last-In-First-Out (LIFO), or some other thread priority scheduling policy. This ensures that the thread which is awakened has the highest priority message to be processed, and enhances the efficiency of message delivery.

15 The priority of the message to be processed is computed based on the overall state of the communication subsystem.



**FIG.1**

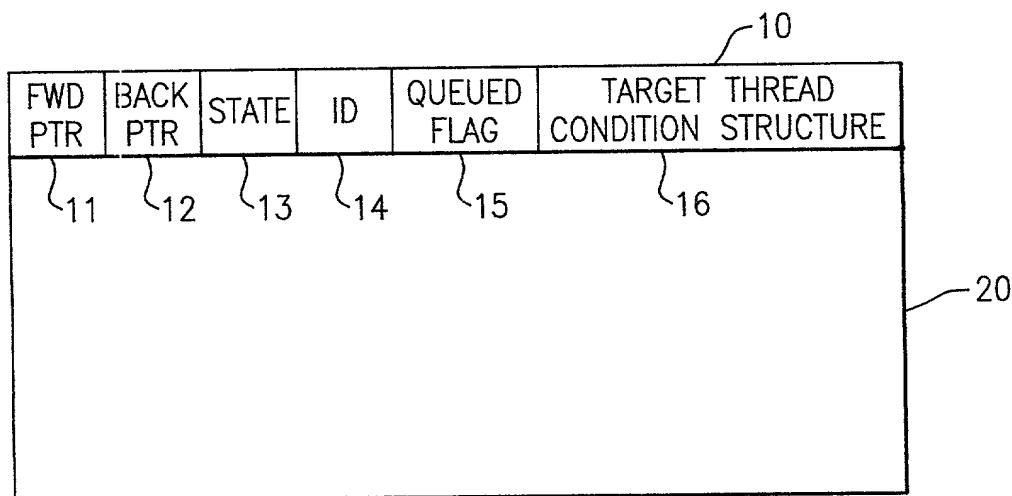


FIG.2

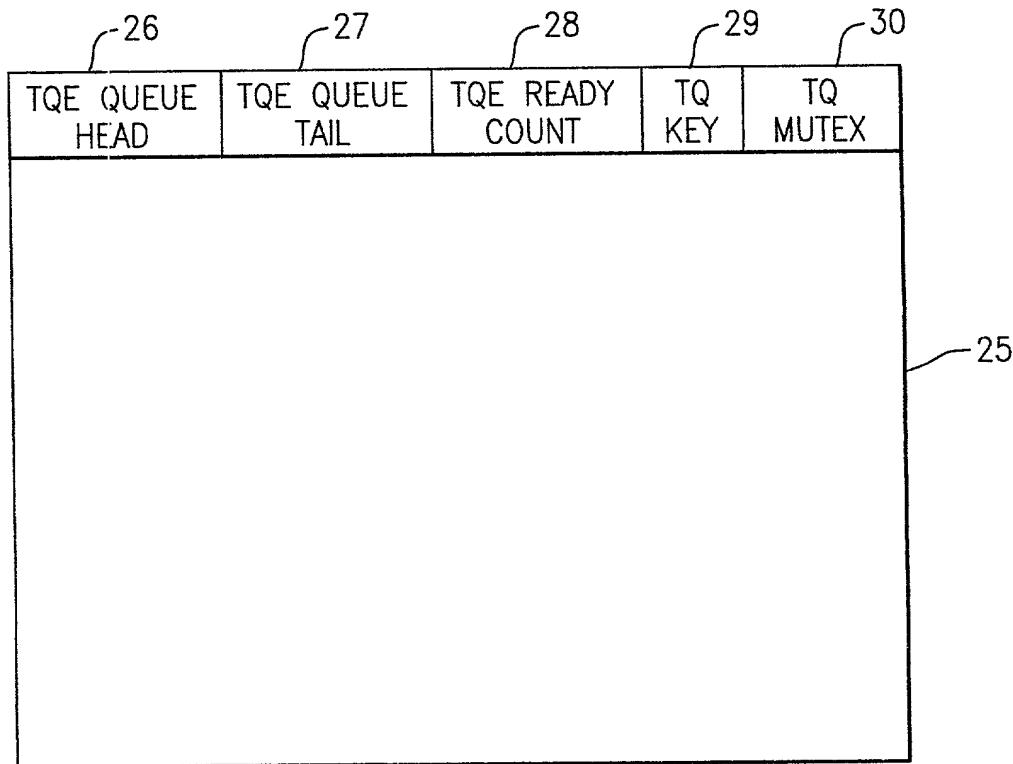
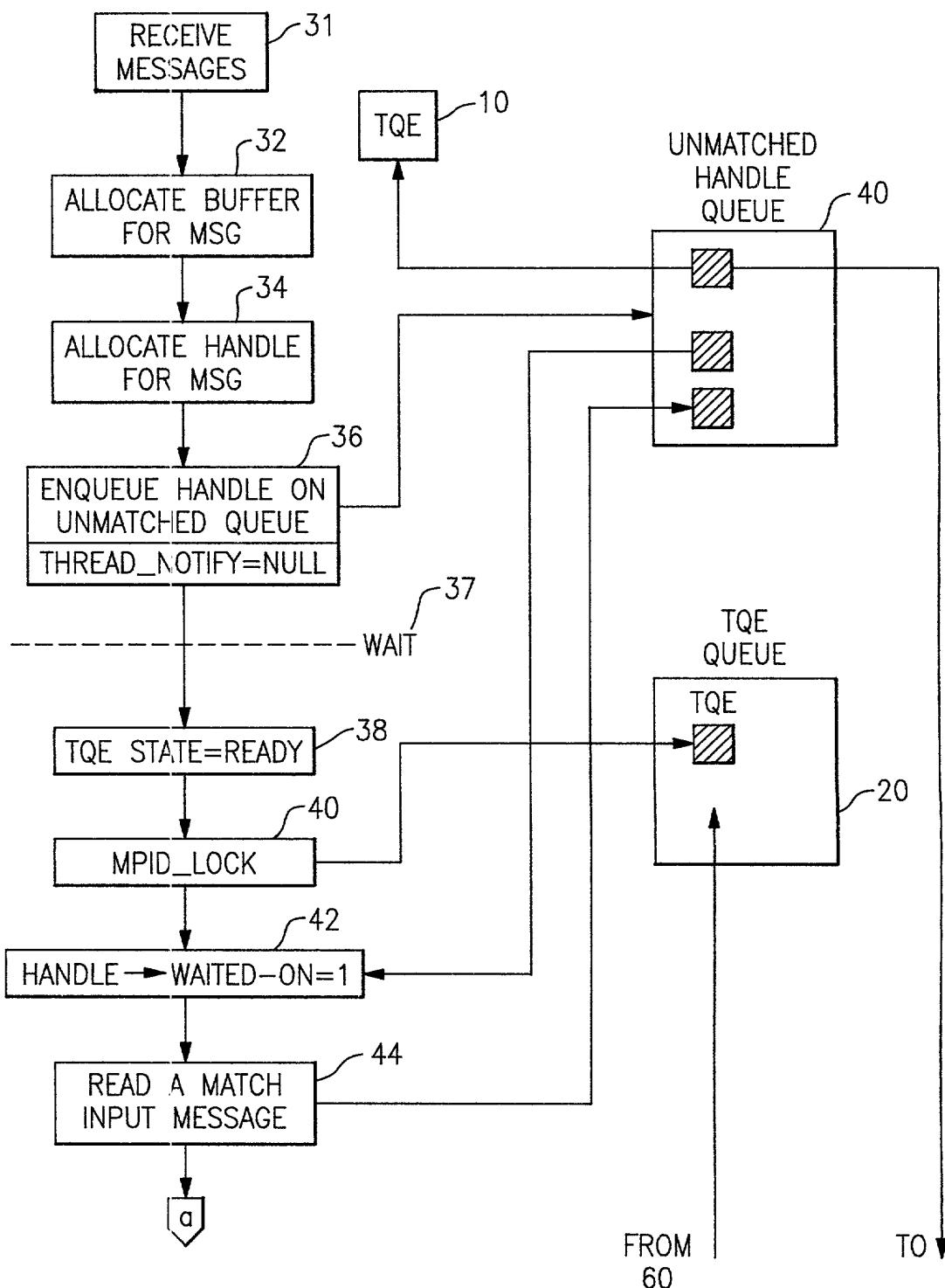


FIG.3

FIG.4A

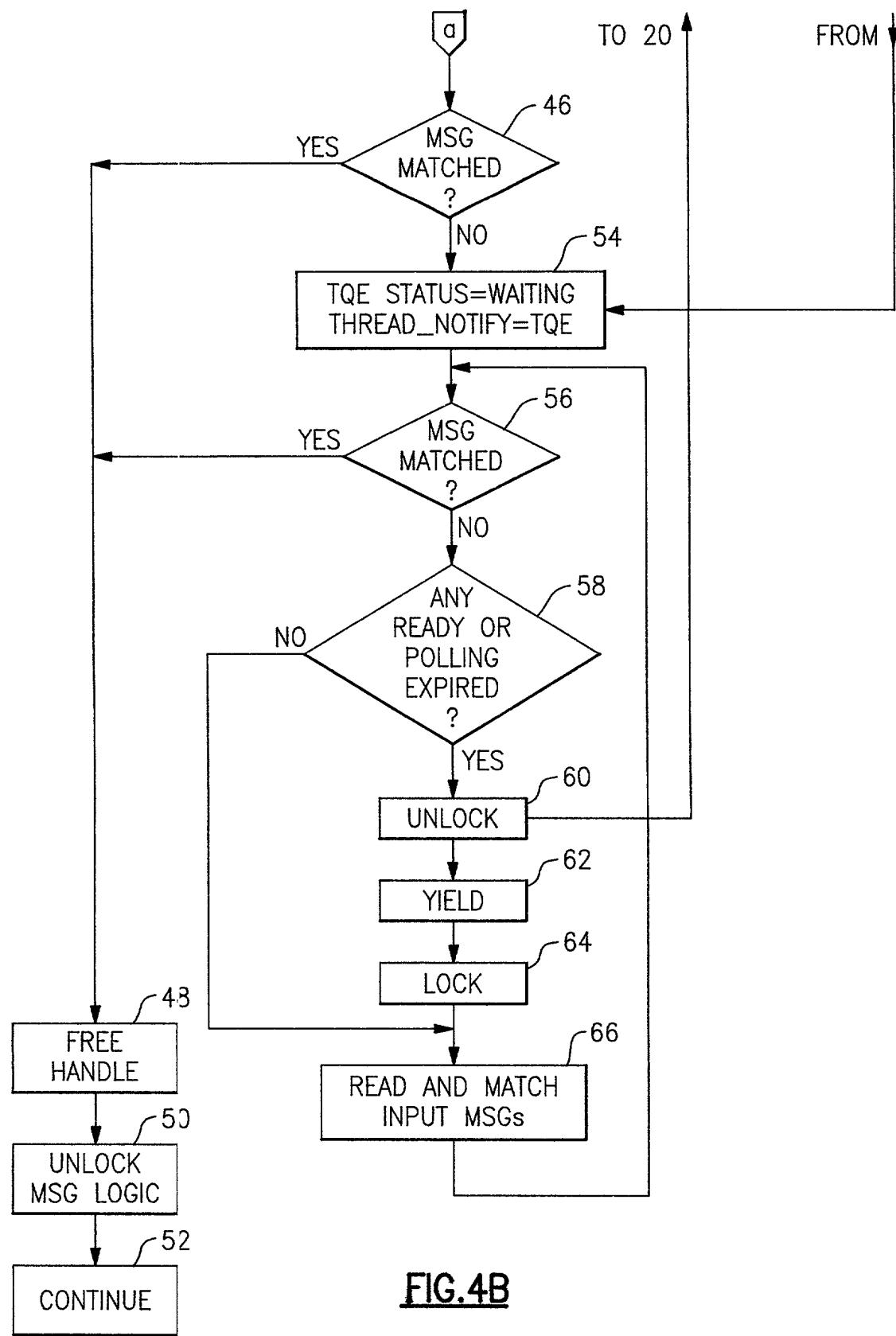
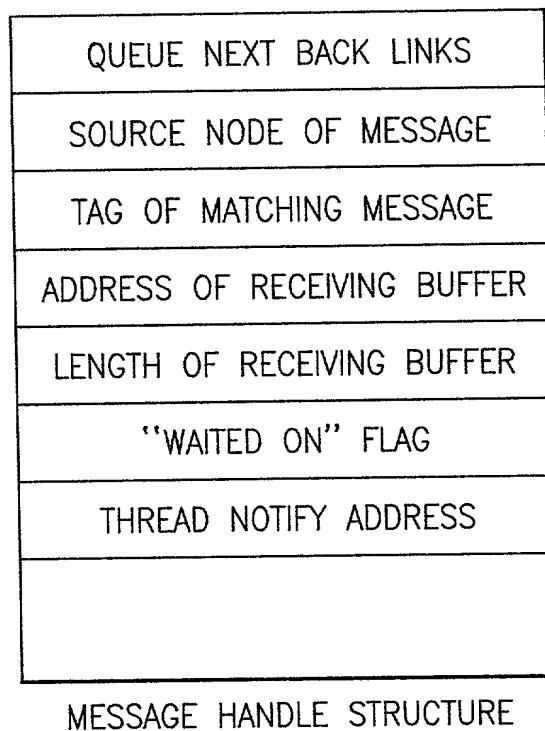
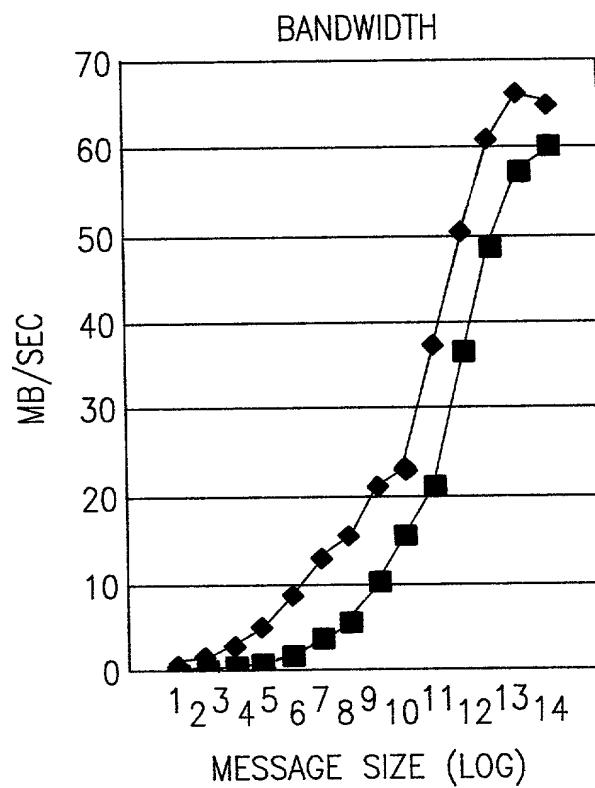
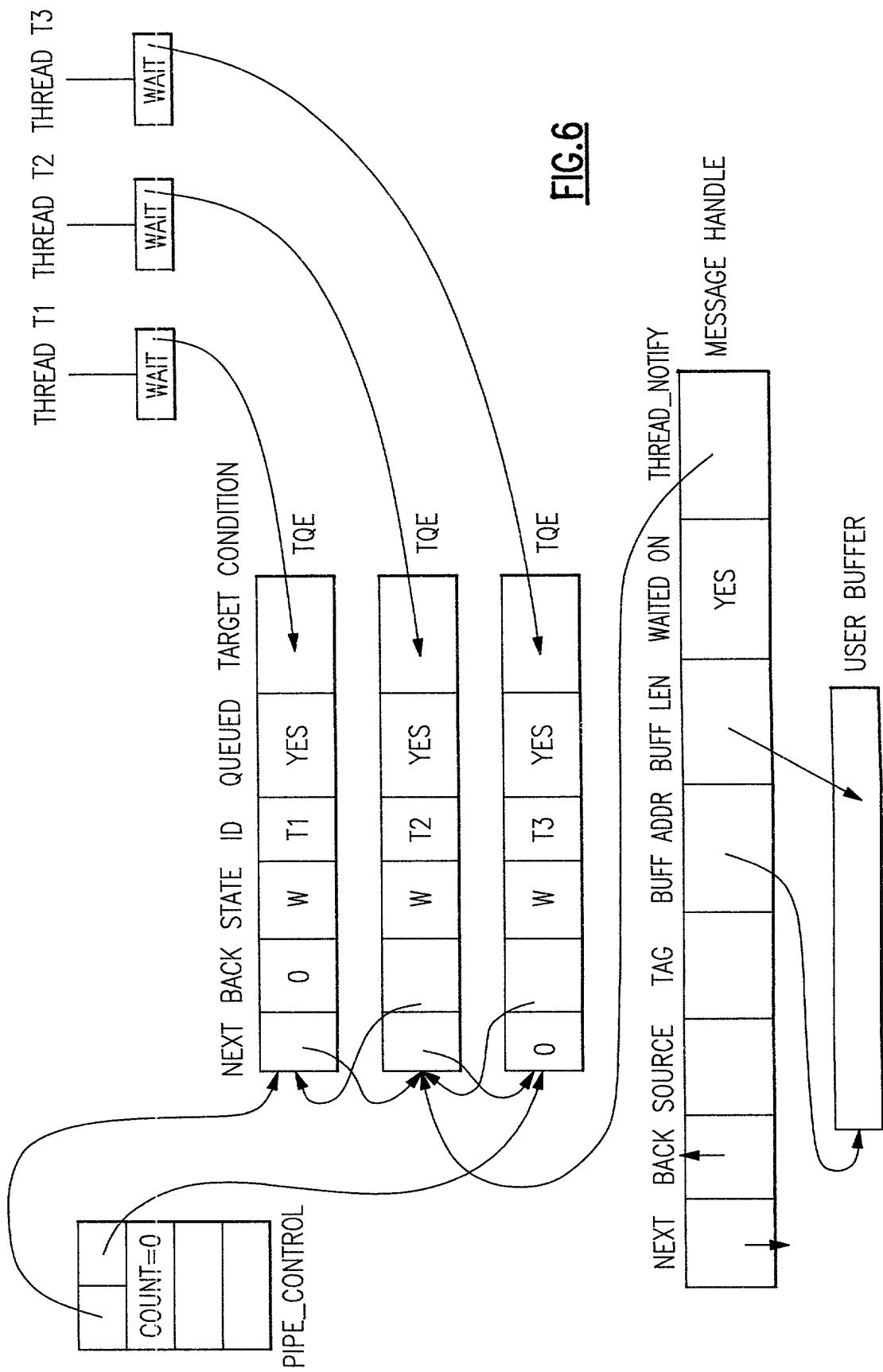


FIG.4B

**FIG.5****FIG.7**



Docket No.  
POU919990100US1

# Declaration and Power of Attorney For Patent Application

## English Language Declaration

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

### THREAD DISPATCHER FOR MULTI-THREADED COMMUNICATION LIBRARY

the specification of which

(check one)

is attached hereto.

was filed on \_\_\_\_\_ as United States Application No. or PCT International

Application Number \_\_\_\_\_

and was amended on \_\_\_\_\_

(if applicable)

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose to the United States Patent and Trademark Office all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, Section 119(a)-(d) or Section 365(b) of any foreign application(s) for patent or inventor's certificate, or Section 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate or PCT International application having a filing date before that of the application on which priority is claimed.

### Prior Foreign Application(s)

### Priority Not Claimed

(Number)	(Country)	(Day/Month/Year Filed)	<input type="checkbox"/>
(Number)	(Country)	(Day/Month/Year Filed)	<input type="checkbox"/>
(Number)	(Country)	(Day/Month/Year Filed)	<input type="checkbox"/>

I hereby claim the benefit under 35 U.S.C. Section 119(e) of any United States provisional

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(Application Serial No.)

(Filing Date)

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(Application Serial No.)

(Filing Date)

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(Application Serial No.)

(Filing Date)

I hereby claim the benefit under 35 U. S. C. Section 120 of any United States application(s), or Section 365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of 35 U.S.C. Section 112, I acknowledge the duty to disclose to the United States Patent and Trademark Office all information known to me to be material to patentability as defined in Title 37, C. F. R., Section 1.56 which became available between the filing date of the prior application and the national or PCT International filing date of this application:

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(Application Serial No.)

(Filing Date)

(Status)  
(patented, pending, abandoned)

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(Application Serial No.)

(Filing Date)

(Status)  
(patented, pending, abandoned)

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(Application Serial No.)

(Filing Date)

(Status)  
(patented, pending, abandoned)

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith. (list name and registration number)

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